

Claims

- [c1] A memory card interface apparatus comprising:
a plurality of memory card interfaces, with at least a subset of the plurality of memory card interfaces configured to interface with a memory card of a first type, the plurality of memory card interfaces accessible in parallel.
- [c2] The apparatus of claim 1, wherein at least one of the memory card interfaces is configured to read a plurality of different memory card types.
- [c3] The apparatus of claim 1, wherein at least one of the memory card interfaces includes an indicator identifying a status of an operation for a respective memory reader interface.
- [c4] The apparatus of claim 3, wherein the indicator includes a light indicating data is being written to a card in the respective memory card interface.
- [c5] The apparatus of claim 1, wherein at least one of the memory card interfaces is configured to interface with a Write Once Read Many (WORM) memory card.
- [c6] The apparatus of claim 1, wherein mechanical pins, of at least one of the plurality of memory card interfaces, are inserted directly into a backbone of the apparatus.
- [c7] The apparatus of claim 1, wherein a first subset of the plurality of memory card interfaces are configured to interface with a memory card of a first type and a second subset of the plurality of memory card interfaces are configured to interface with a memory card of a second type, wherein the first and second subset of memory card interfaces are accessible in parallel.
- [c8] The apparatus of claim 1, wherein the apparatus includes a text display, wherein text on the display is manipulated using Simple Display Device commands.
- [c9] The apparatus of claim 1, wherein the apparatus includes a text display, wherein text on the display is manipulated using Small Computer System Interface commands.

- [c10] A system comprising:
a controller circuit;
a bus coupled to the controller circuit; and
a plurality of memory card interfaces, with at least a subset of the plurality of memory card interfaces configured to interface with a memory card of a first type, the plurality of memory card interfaces accessible in parallel.
- [c11] The system of claim 10, wherein at least one of the memory card interfaces is configured to interface with a plurality of different memory card types.
- [c12] The system of claim 10, wherein at least one of the memory card interfaces includes an indicator identifying a status of an operation for a respective memory card interface.
- [c13] The system of claim 12, wherein the indicator includes a light indicating data is being written to a card in the respective memory card interface.
- [c14] The system of claim 10, wherein at least one of the memory card interfaces is configured to interface with a Write Once Read Many (WORM) memory card.
- [c15] The system of claim 10, wherein mechanical pins, of at least one of the plurality of memory card interfaces, are inserted directly into a backbone of the system.
- [c16] The system of claim 10, wherein a first subset of the plurality of memory card interfaces are configured to interface with a memory card of a first type and a second subset of the plurality of memory card interfaces are configured to interface with a memory card of a second type, wherein the first and second subset of memory card reader interfaces are accessible in parallel.
- [c17] The system of claim 10, wherein the system includes a text display, wherein text on the display is manipulated using Simple Display Device commands.
- [c18] The system of claim 10, wherein the system includes a text display, wherein text on the display is manipulated using Small Computer System Interface commands.
- [c19] A method comprising:

